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Total Number of Pages in This Submission

Application Number	09/905,187	
Filing Date	July 12, 2001	
First Named Inventor	Richard C. Eden et al.	8
Group Art Unit	2182	_
Examiner Name	Unknown	9 20
Attorney Docket Number	IS9-018	2001

ENCLOSURES (check all that apply)					
Fee Transmittal Form	Assignment Papers (for an Application)	After Allowance Communication to Group Appeal Communication to Board			
Amendment / Response After Final Affidavits/declaration(s) Extension of Time Request	Drawing(s) Licensing-related Papers Petition Routing Slip (PTO/SB/69) and Accompanying Petition Petition to Convert to a Provisional Application Power of Attorney, Revocation Change of Correspondence	of Appeals and Interferences Appeal Communication to Group (Appeal Notice, Brief, Reply Brief) Proprietary Information Status Letter Additional Enclosure(s)			
Express Abandonment Request X Information Disclosure Statement Certified Copy of Priority Document(s) Response to Missing Parts/ Incomplete Application Response to Missing Parts under 37 CFR 1.52 or 1.53	Address Terminal Disclaimer Small Entity Statement	PTO Postcard Receipt Form PTO-1449 Cited References			
SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT					
Firm or Individual name James D. Shaurette, Reg. No. 39,833 Wells, St. John, Roberts, Gregory & Matkin, P.S. Signature					
Date 10/12/01					
CERTIFICATE OF MAILING					

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on this date: $10^{-1/2}-01$

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Application Serial No
Filing Date July 12, 2001
Inventor Richard C. Eden et al.
Assignee Isothermal Systems Research, Inc.
Group Art Unit
Examiner Unknown
Attorney's Docket No IS9-018
Title: Power Semiconductor Switching Devices, Power Converters, Integrated Circuit
Assemblies, Integrated Circuitry, Power Current Switching Methods, Methods of Forming
a Power Semiconductor Switching Device, Power Conversion Methods, Power
Semiconductor Switching Device Packaging Methods, and Methods of Forming a Power
Transistor

INFORMATION DISCLOSURE STATEMENT

References -- See Attached Form PTO-1449

Assistant Commissioner for Patents Washington, D.C. 20231

The attached form PTO-1449 is submitted in compliance with 37 CFR §1.56. No admission is made regarding whether all the submitted references are prior art.

Date:

Ву:

Respectfully submitted,

James D. Shaurette Reg. No. 39,833

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